

## CLAIMS

1. A spindle motor control circuit for controlling a motor; comprising;  
a control circuit to control said motor during at least a low state, a pulse state and  
a high state;

sub B1  
said motor braking during said low state;

said control circuit receiving a flyback voltage from said motor during said pulse  
state;

said control circuit receiving a reduced flyback voltage from said motor during  
said high state.

2. A spindle motor control circuit for controlling a motor, as in Claim 1,  
wherein said control circuit includes an op amp to feed back a voltage to limit sent  
flyback voltage from said motor.

3. A spindle motor control circuit for controlling a motor, as in Claim 1,  
wherein said voltage is a first voltage during said pulse state and a second voltage  
during said high state.

4. A spindle motor control circuit for controlling a motor, as in Claim 3,  
wherein said first voltage is greater than said second voltage.

sub B2  
5. A spindle motor control circuit for controlling a motor, as in Claim 1,  
wherein said motor is braked before said pulse state and after said high state.